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VLSI Digital Signal Processing Systems Lan-Da Van VLSI-DSP-0-12 My Lab: VLSI Information Processing (VIP) Research Lab Lan-Da Van's research interests are in VLSI algorithms, architectures, and chips for digital signal processing (DSP) and biomedical signal processing

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550 papers, has authored the textbook VLSI Digital Signal Processing Systems (Wiley, 1999) and co-edited the reference book Digital Signal Processing for Multimedia Systems (Marcel Dekker, 1999) Dr Parhi is widely recognized for his work on high-level transformations of iterative data-flow computations and for developing a formal theory of

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REQUIRED: Keshab K Parhi, VLSI Digital Signal Processing Systems: Design and Implementation, Wiley, 1999, ISBN: 978-0471241867 REQUIRED: MATLAB and Simulink Student Version Release 2013a, English Version ISBN: 978-0-9825838-8-3 Supplementary Reading: 1 Shoab Ahmed Khan, Digital Design of Signal Processing Systems: A Practical Approach,

### **Iteration Bound - TUIASI**

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Clocks in Digital Systems •Why are clocks and clocked memory registers needed inside digital systems? •Clocks pace the flow of data inside digital processors •The exact speed of data through circuits is impossible VLSI Digital Signal Processing Author: Bevan Baas

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